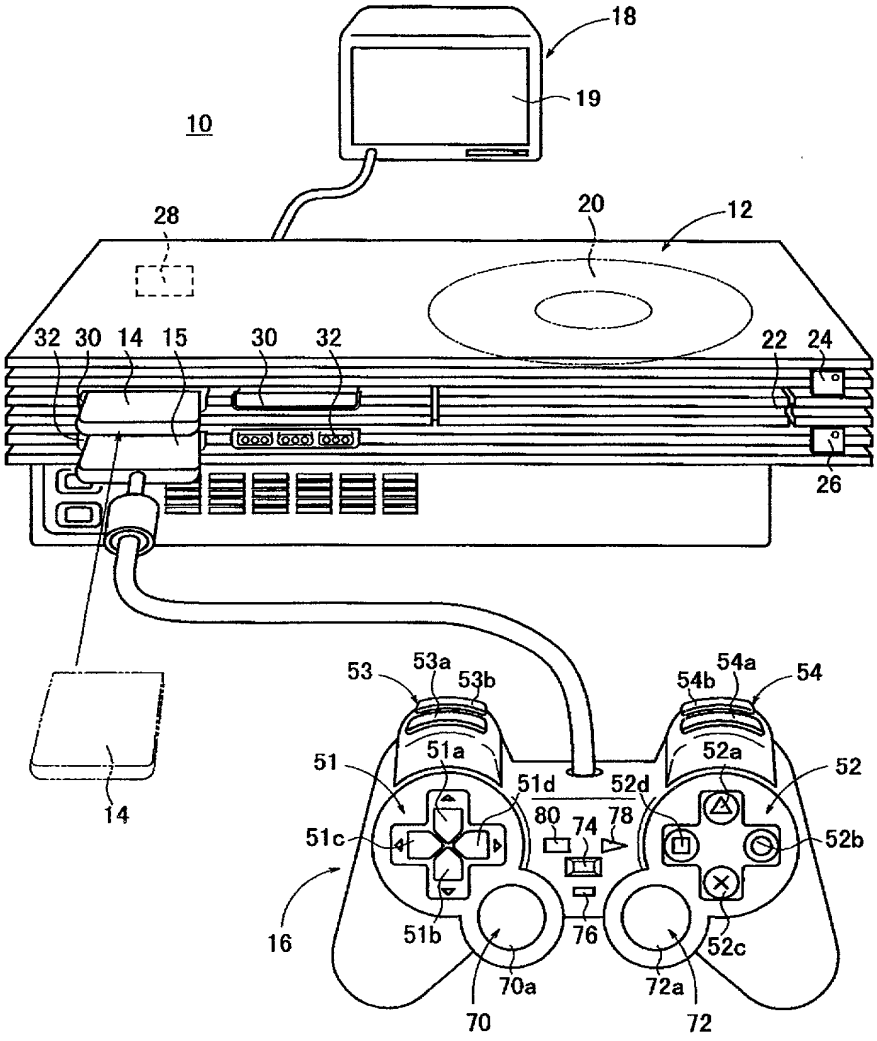
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19
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32
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14
15
30
32
22
24
26
14
53
53a
53b
54b
54a
54
51
51a
52a
52
51d
52d
80
74
78
51c
52b
16
51b
76
52c
70a
72a
70
72

FIG. 1

Document: 2007-0000000

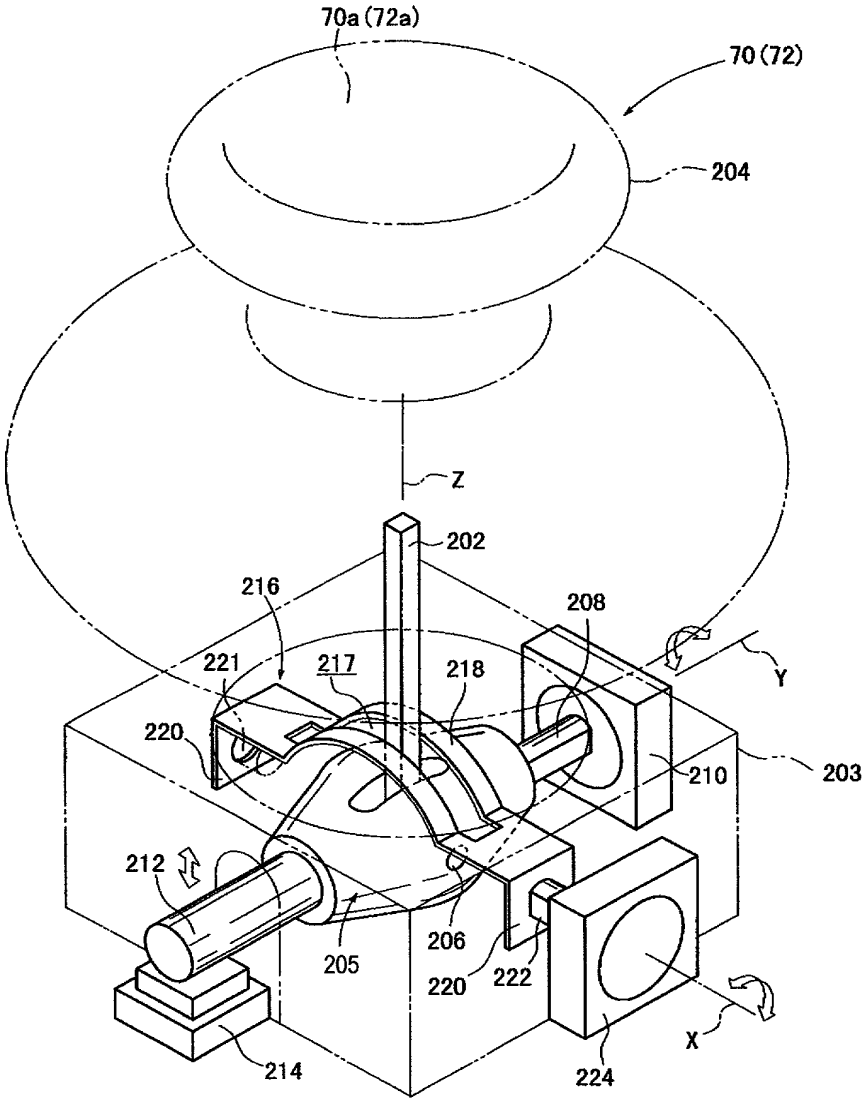


FIG. 2

Machine Translation

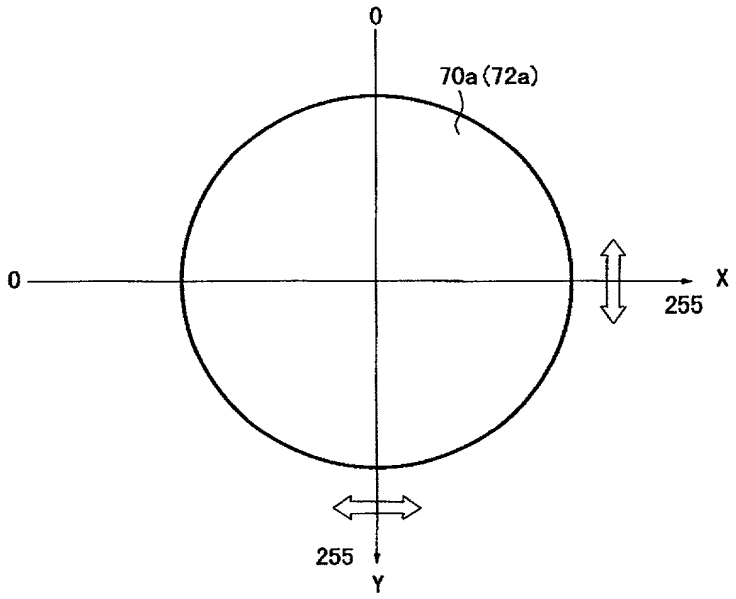


FIG. 3

Patent Application Publication

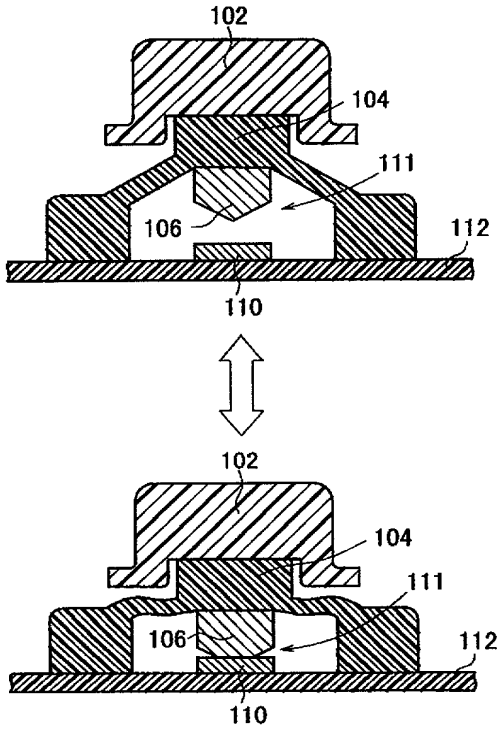
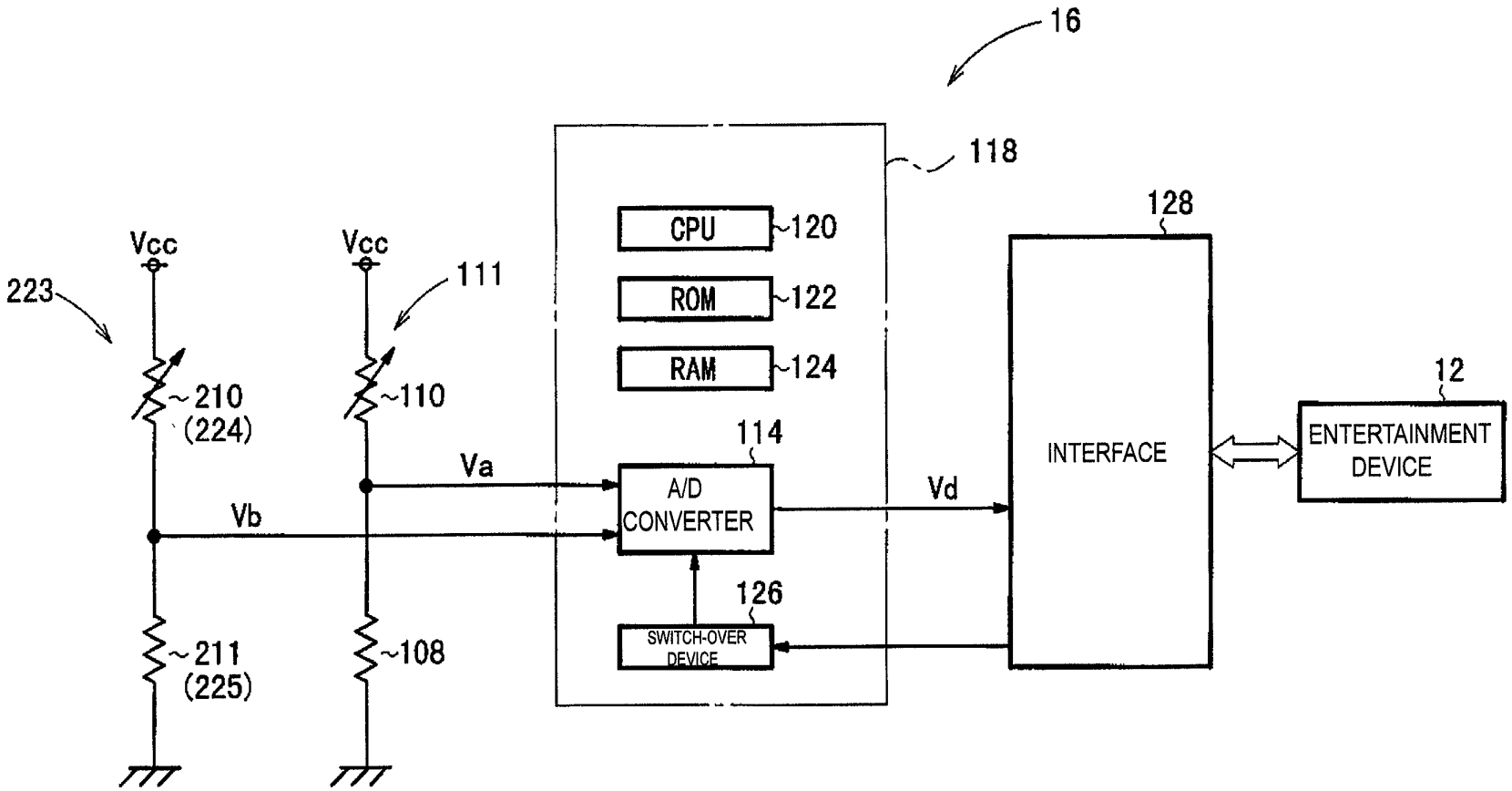


FIG. 4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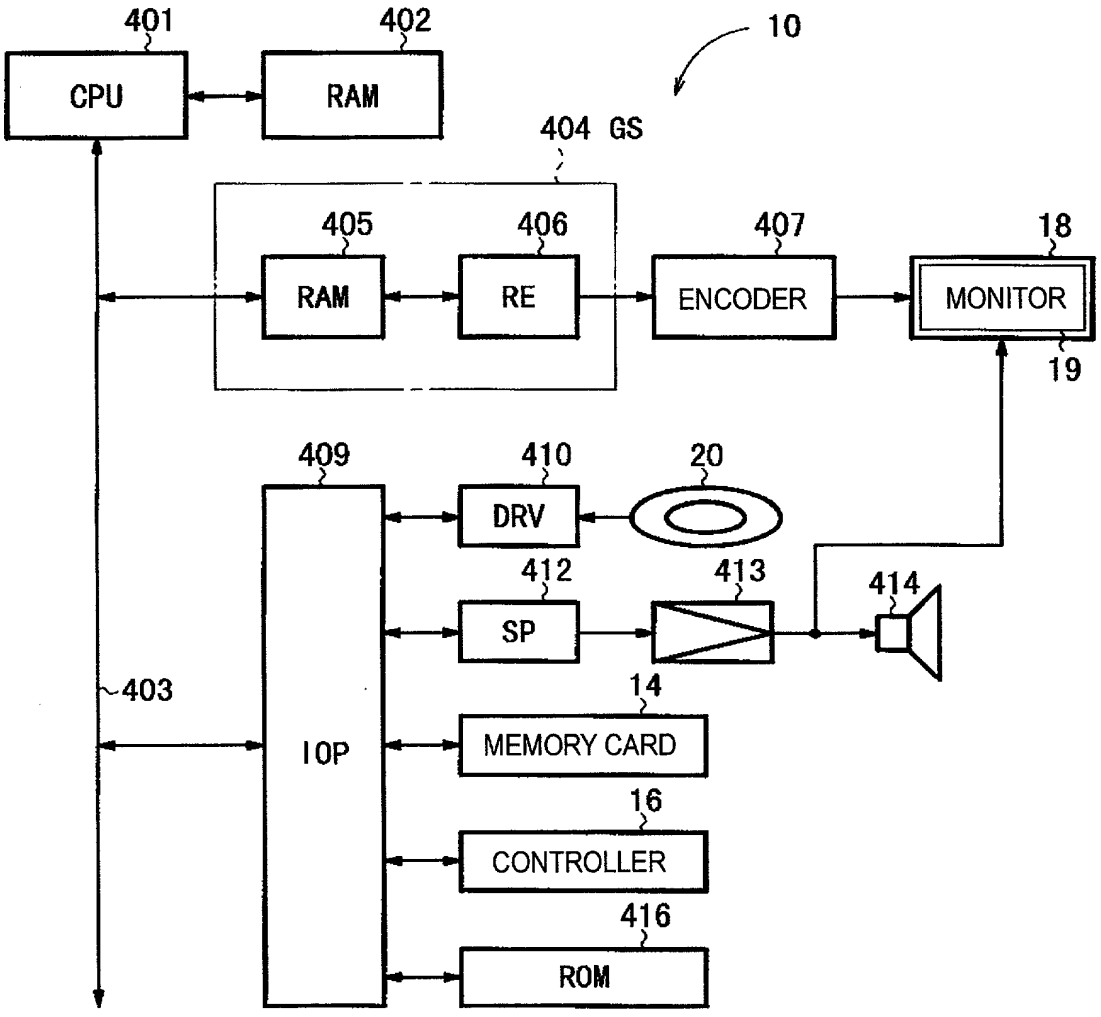
118
CPU
120
ROM
122
RAM
124
128
12
INTERFACE
ENTERTAINMENT DEVICE
Vcc
Vcc
223
111
210
(224)
110
Va
114
A/D CONVERTER
Vd
Vb
126
SWITCH-OVER DEVICE
211
(225)
108

FIG. 5


401
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CPU
RAM
404 GS
405
406
407
18
RAM
RE
ENCODER
MONITOR
19
409
410
20
DRV
412
413
414
SP
403
14
IOP
MEMORY CARD
16
CONTROLLER
416
ROM

FIG. 6

Patent Application Publication

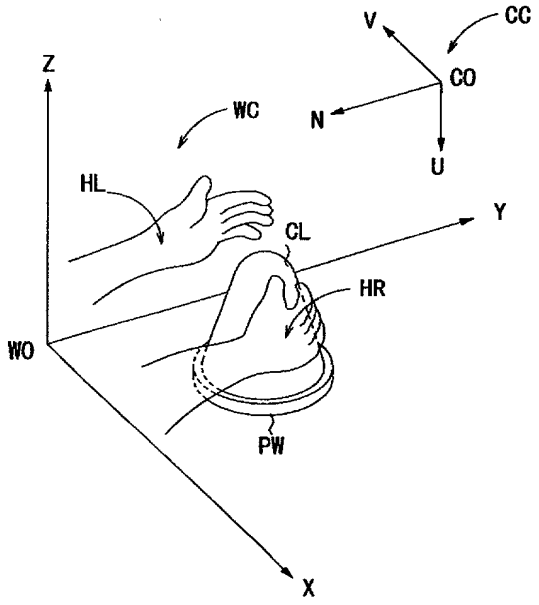

FIG. 7

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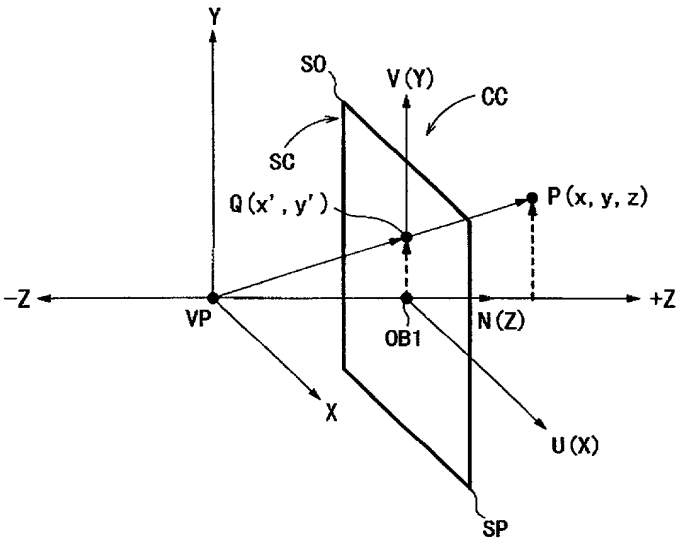

FIG. 8

230

OPERATING OUTPUT VALUE Vb	PERCENTAGE R $\left(\frac{\text{MAGNITUDE OF COMMANDED FORCE}}{\text{MAXIMUM VALUE THAT CAN BE COMMANDED}}\right)$
0	1
1	$\frac{126}{127}$
126	$\frac{1}{127}$
127	0
128	$\frac{1}{128}$
254	$\frac{127}{128}$
255	1

FIG. 9

232

OPERATING OUTPUT VALUE Va	PERCENTAGE R $\left(\frac{\text{MAGNITUDE OF COMMANDED FORCE}}{\text{MAXIMUM VALUE THAT CAN BE COMMANDED}}\right)$
0	1
1	$\frac{1}{255}$
254	$\frac{254}{255}$
255	1

FIG. 10

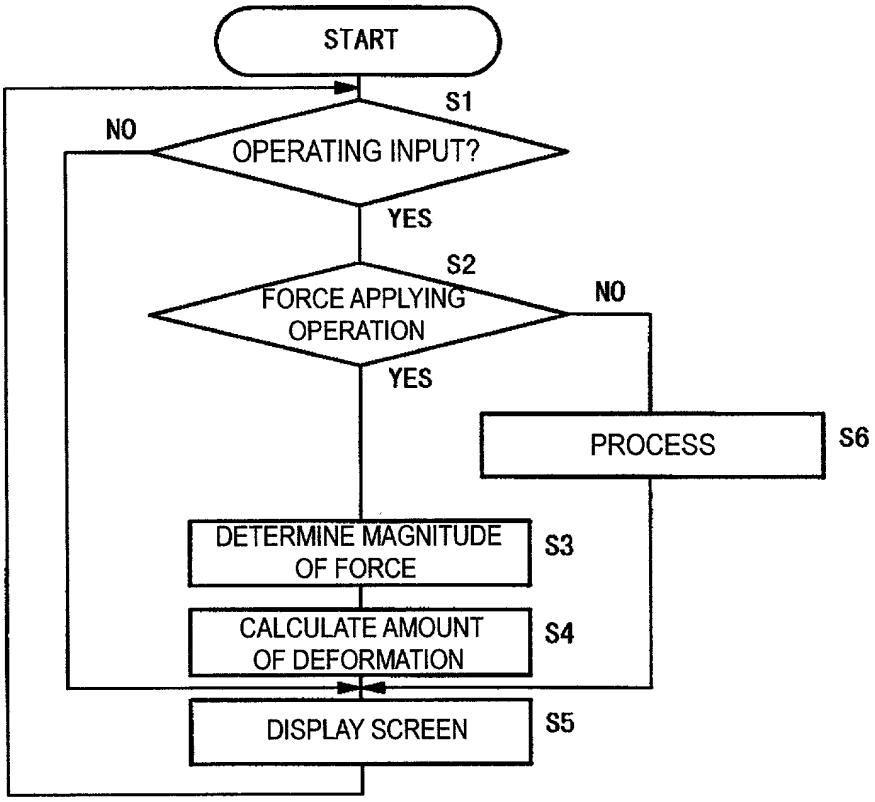
START
S1
NO
OPERATING INPUT?
YES
S2
FORCE APPLYING OPERATION
NO
YES
PROCESS
S6
DETERMINE MAGNITUDE OF FORCE
S3
CALCULATE AMOUNT OF DEFORMATION
S4
DISPLAY SCREEN
S5

FIG. 11

Document ID: US 2022/0000000 A1

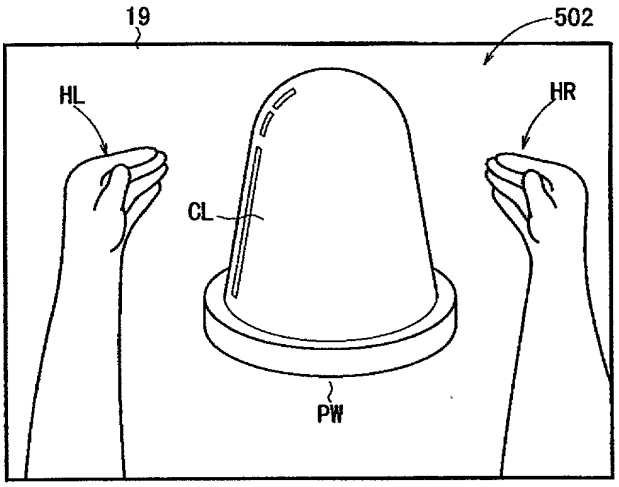

FIG. 12

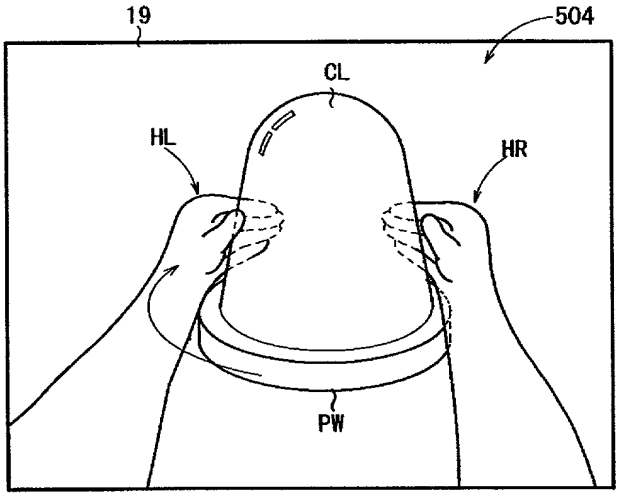

FIG. 13

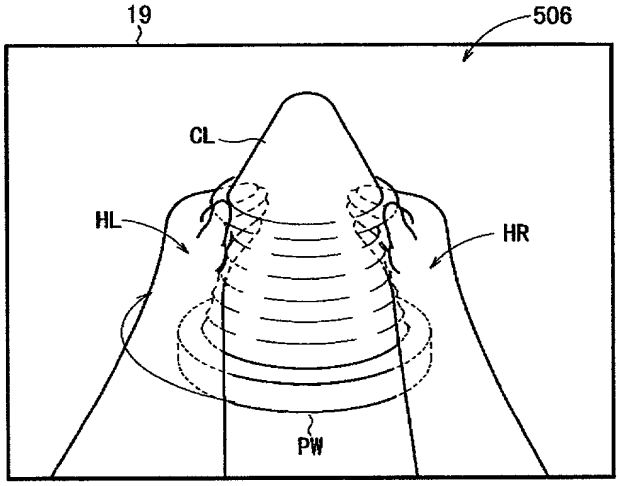

FIG. 14

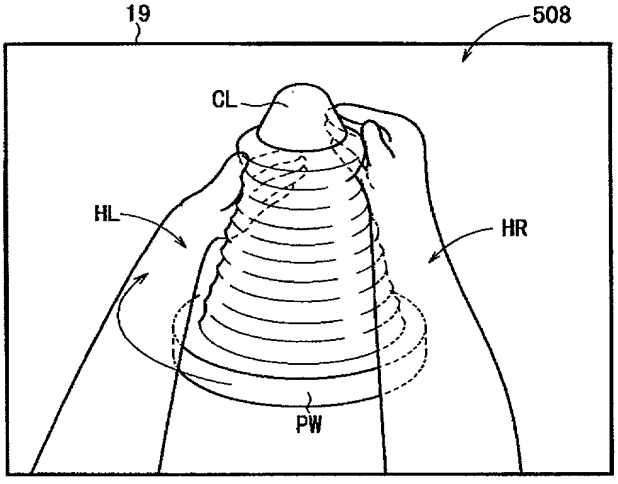

FIG. 15

Document: 2007/0216642

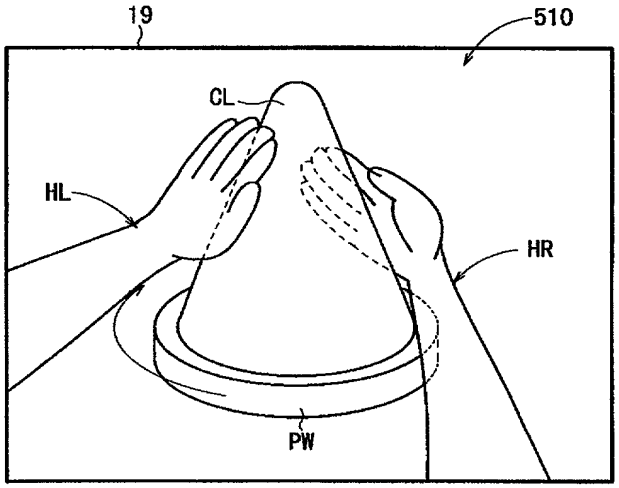

FIG. 16

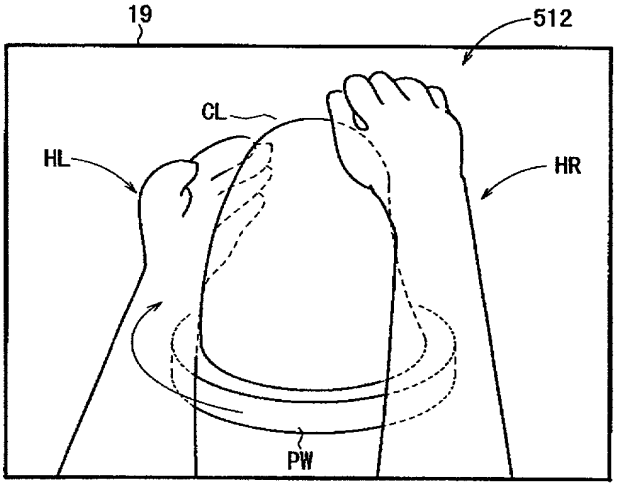

FIG. 17

Patent Application Publication

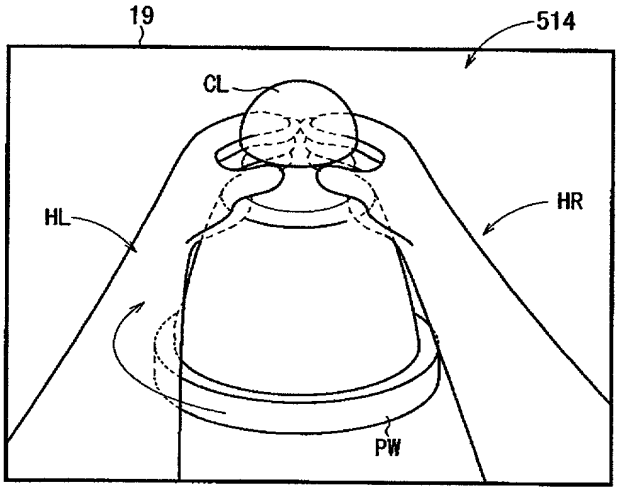

FIG. 18

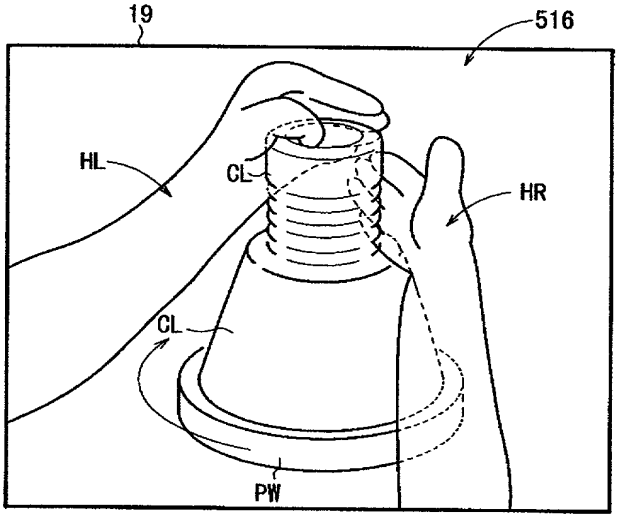

FIG. 19

Document: EP 2 216 807 A1

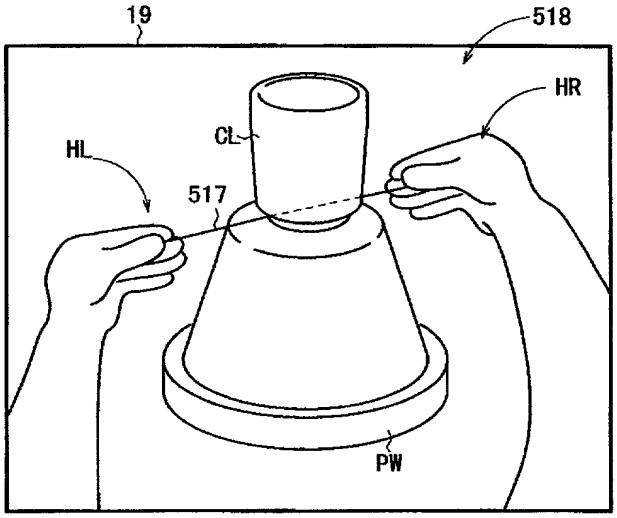

FIG. 20

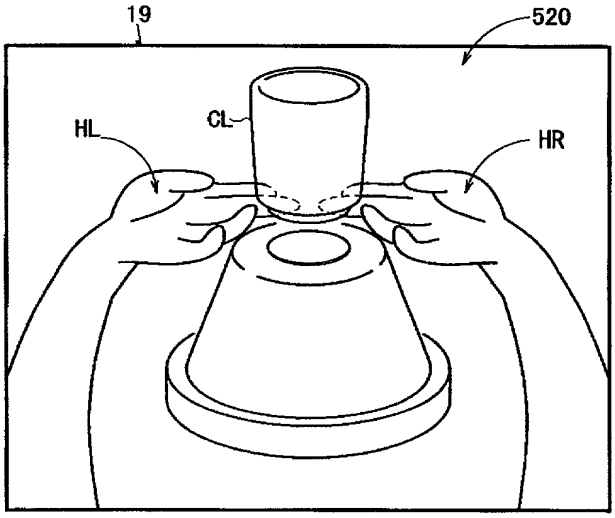

FIG. 21

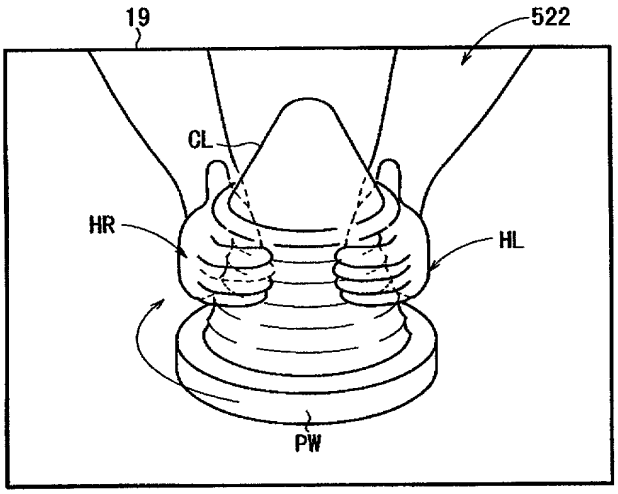

FIG. 22

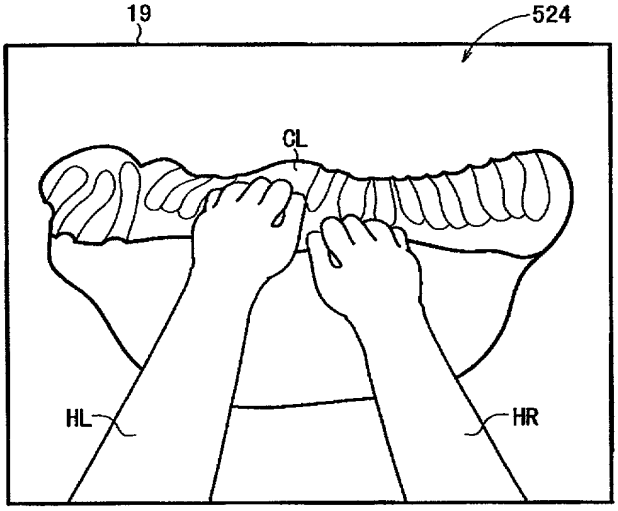

FIG. 23

Appendix B: Page 6 of 8

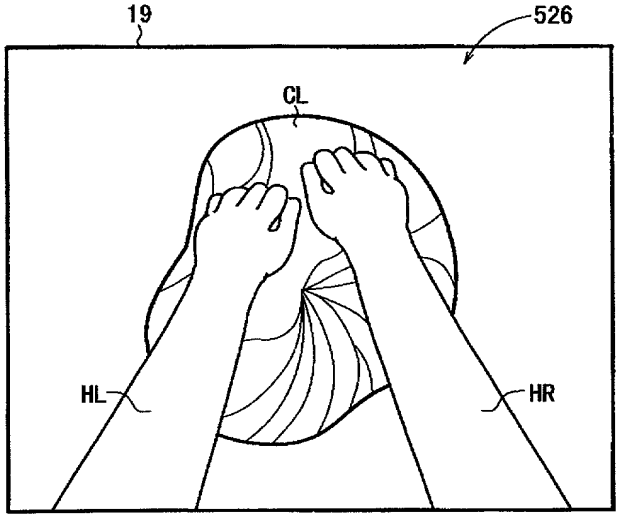

FIG. 24

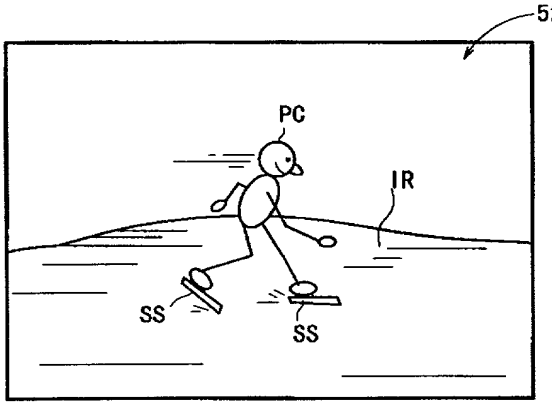

FIG. 25

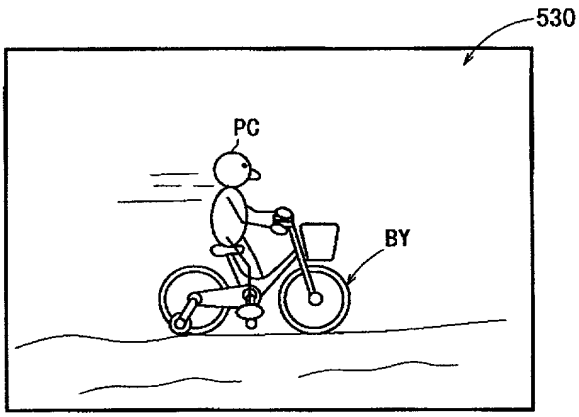

FIG. 26

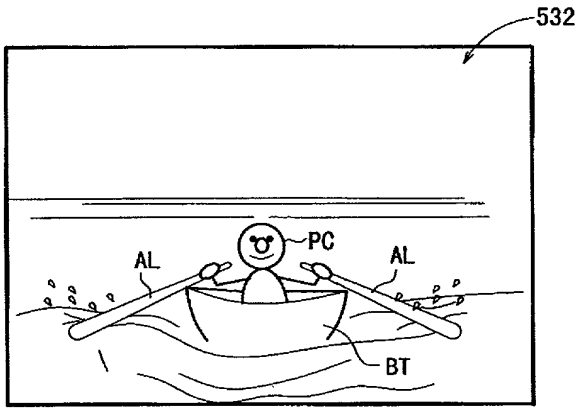

FIG. 27